

FIG. 1

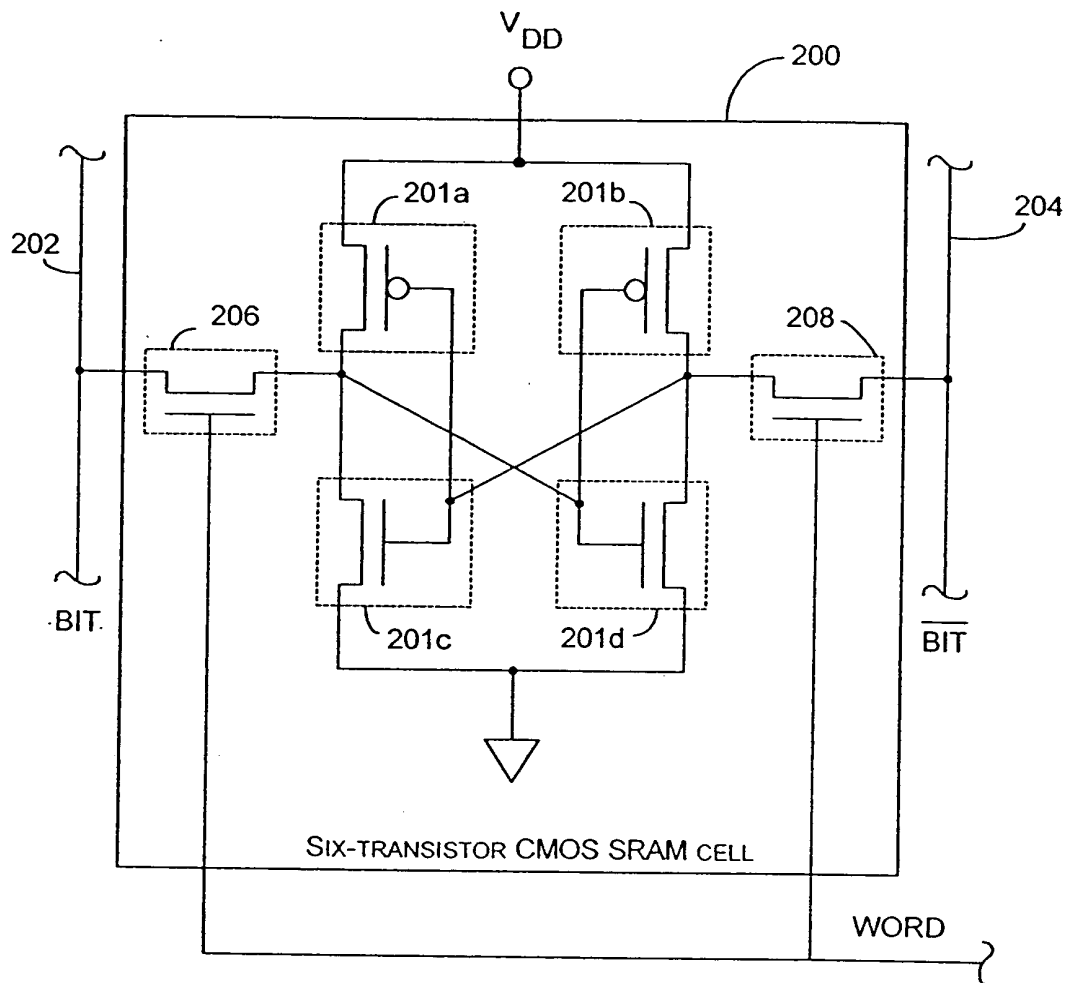


FIG. 2

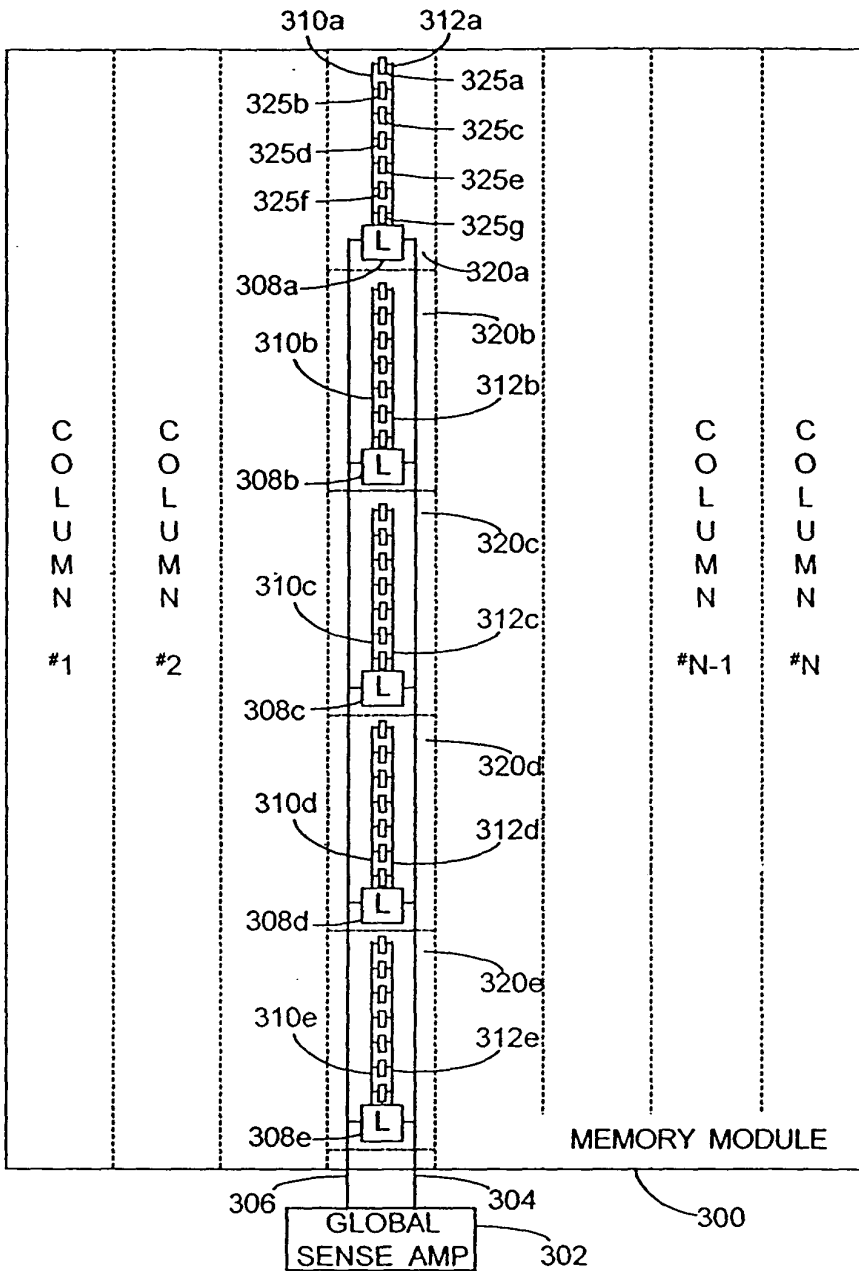


FIG. 3

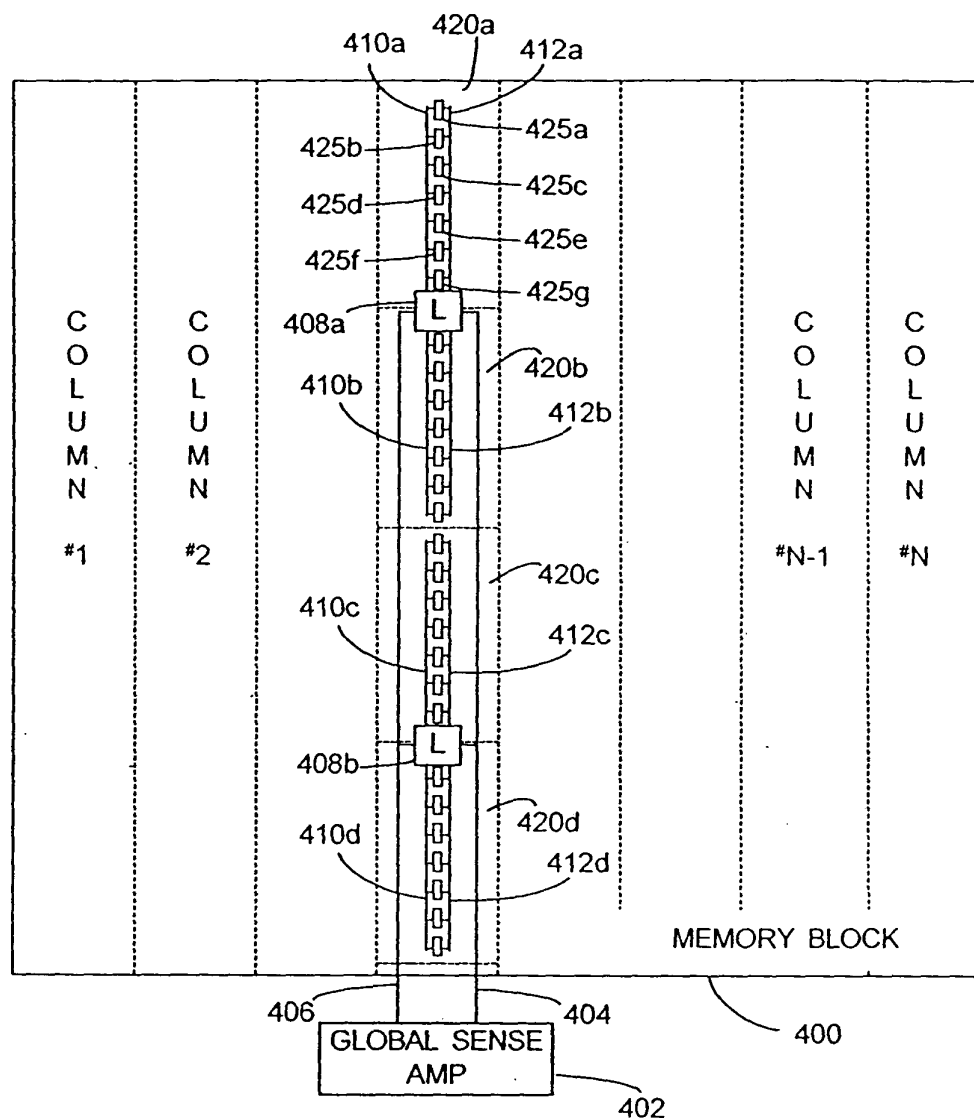
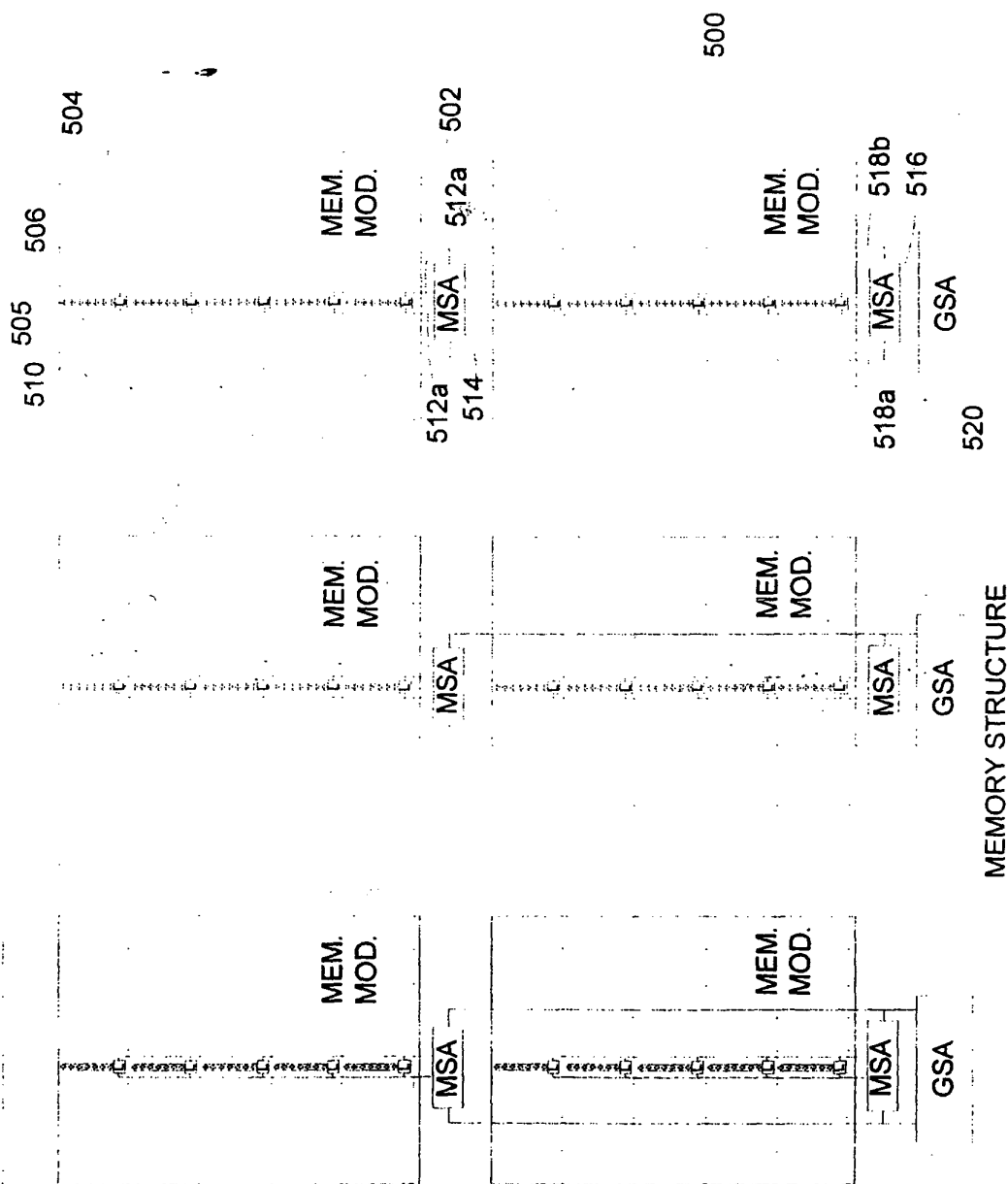


FIG. 4

Superseded  
 by Fig. 5  
 attached to  
 Preliminary Amdmt



MEMORY STRUCTURE

FIG. 5

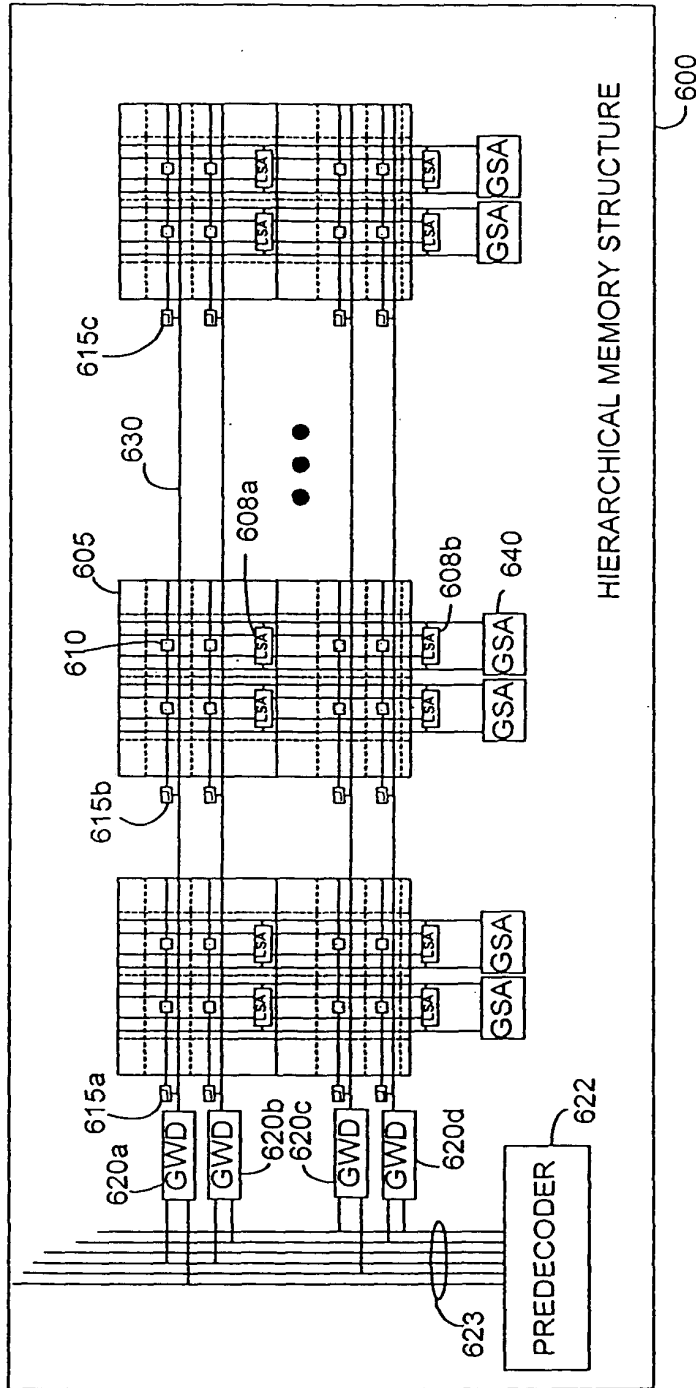
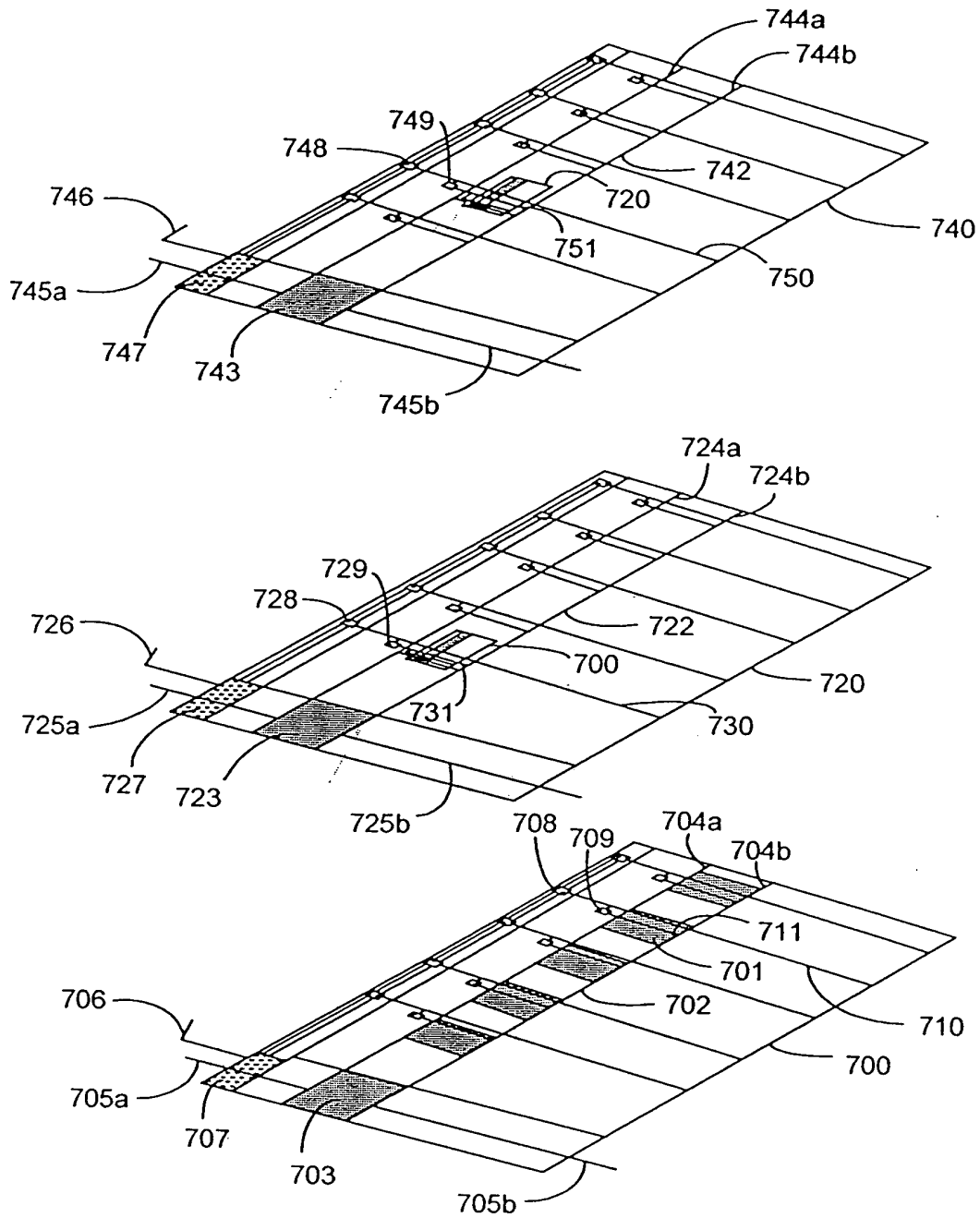
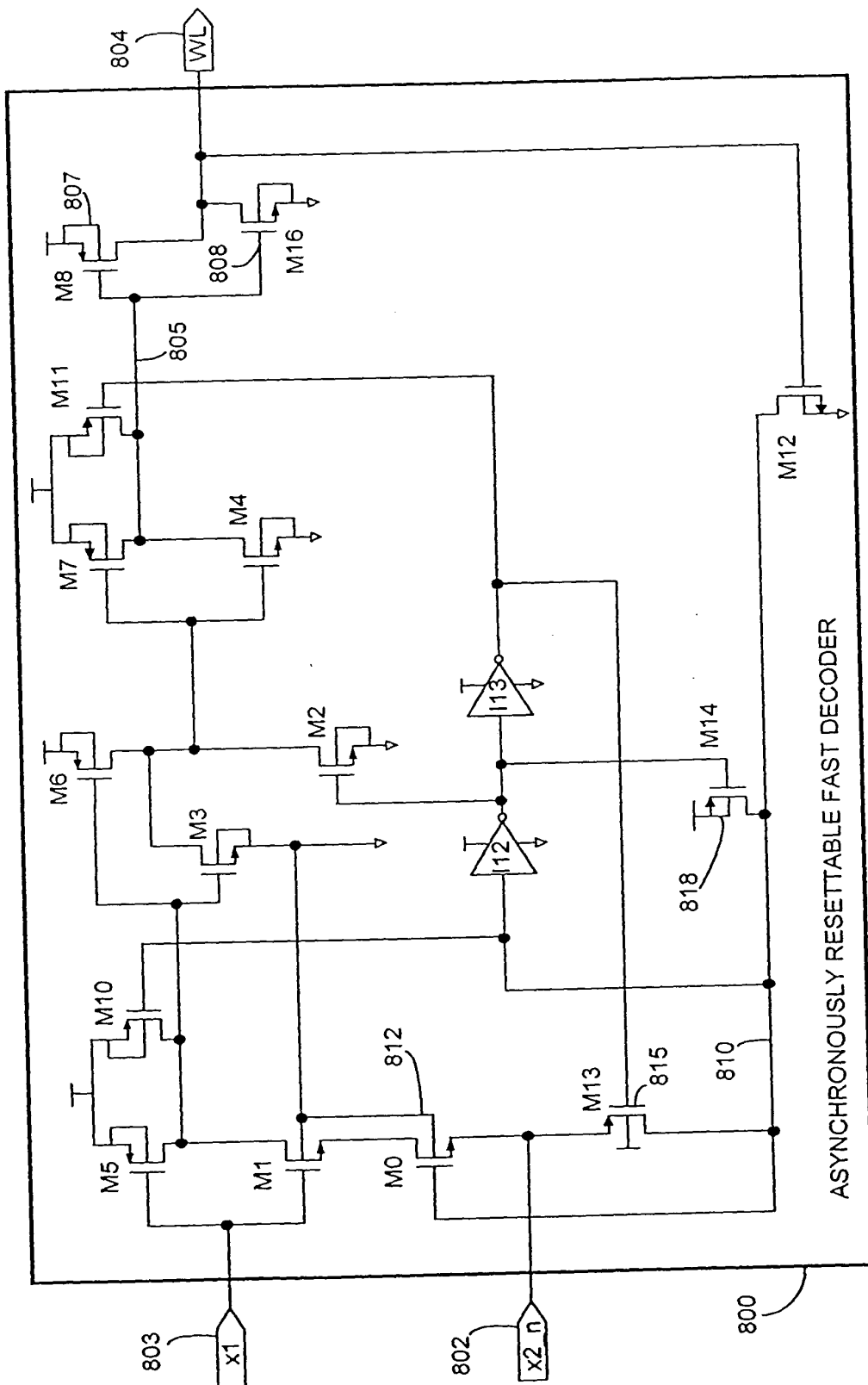


FIG. 6



**FIG. 7**



**FIG. 8**

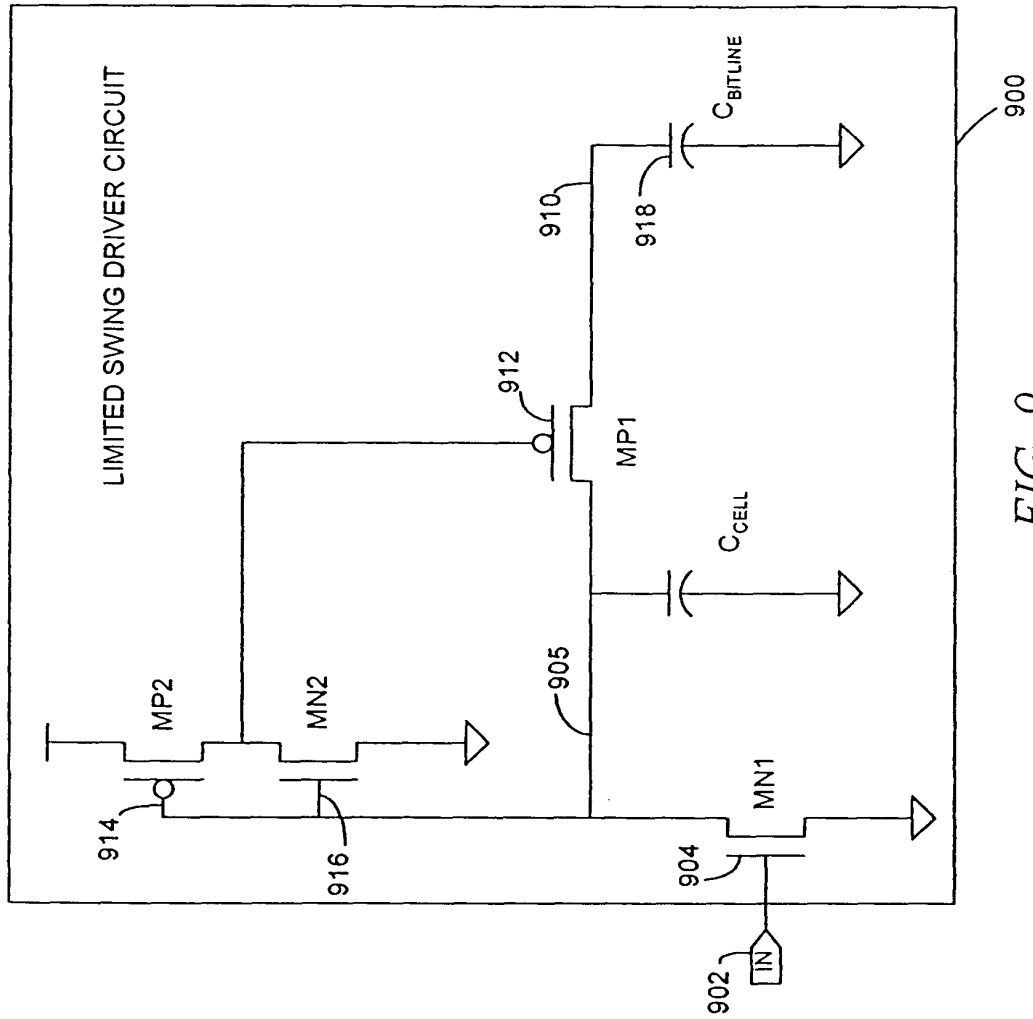


FIG. 9

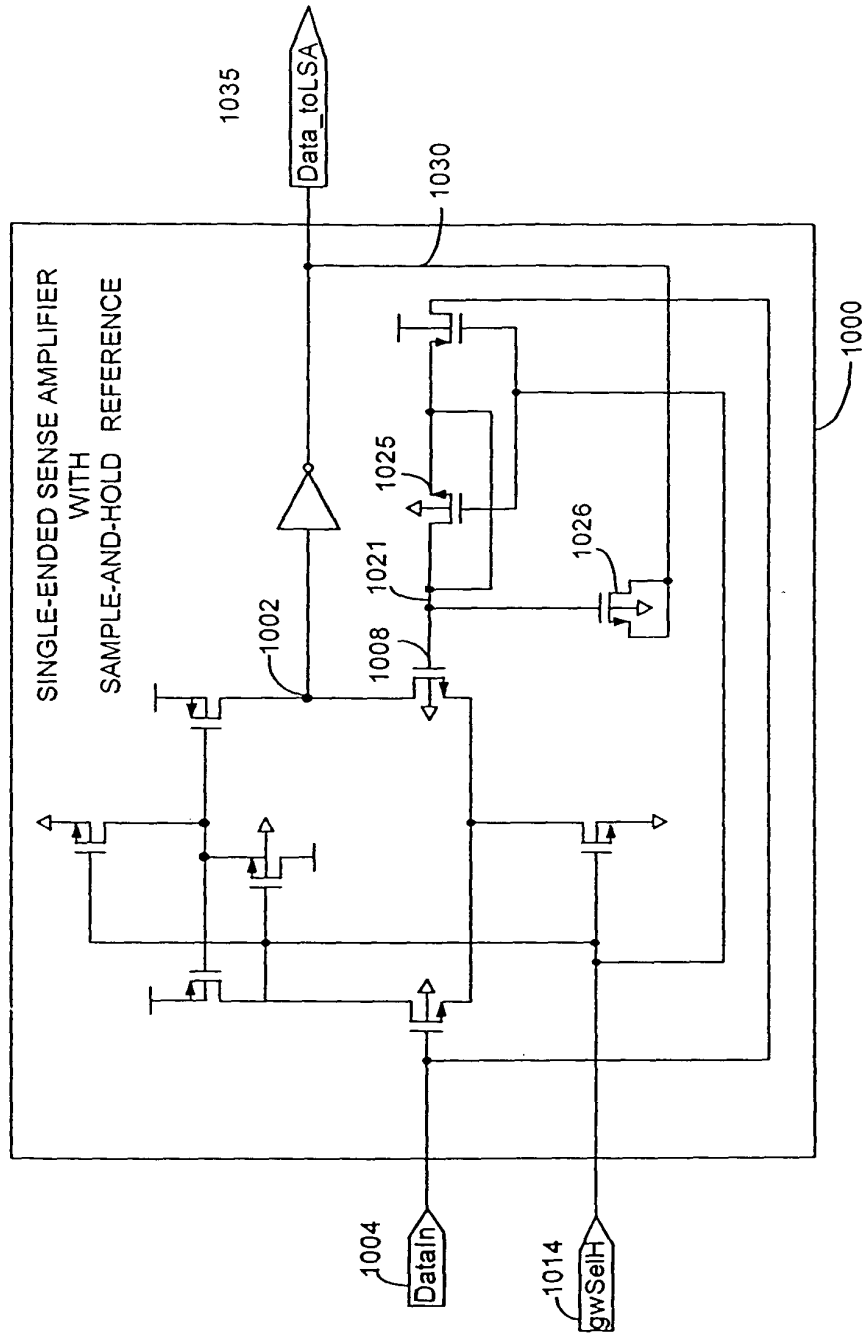


FIG. 10

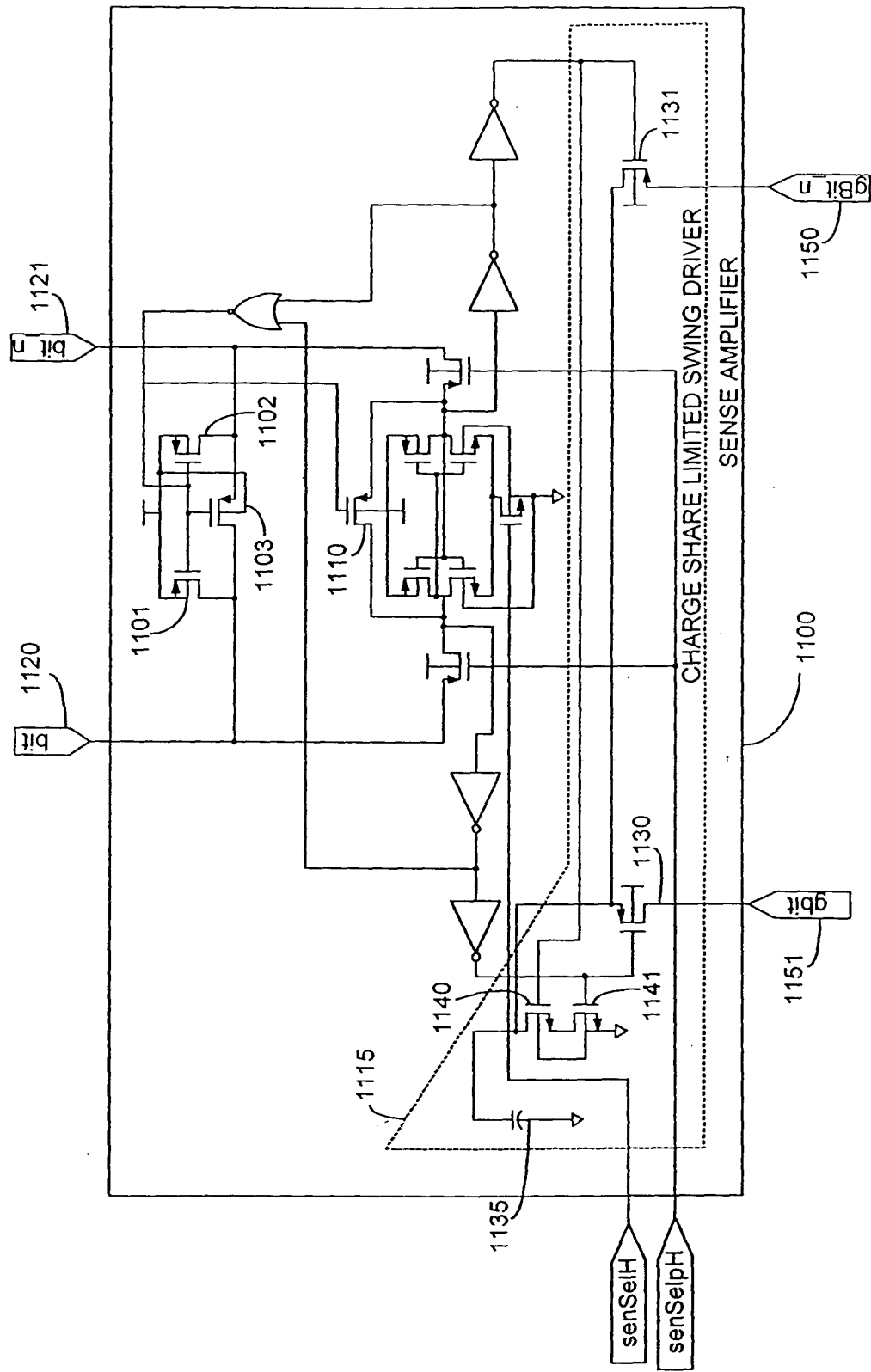
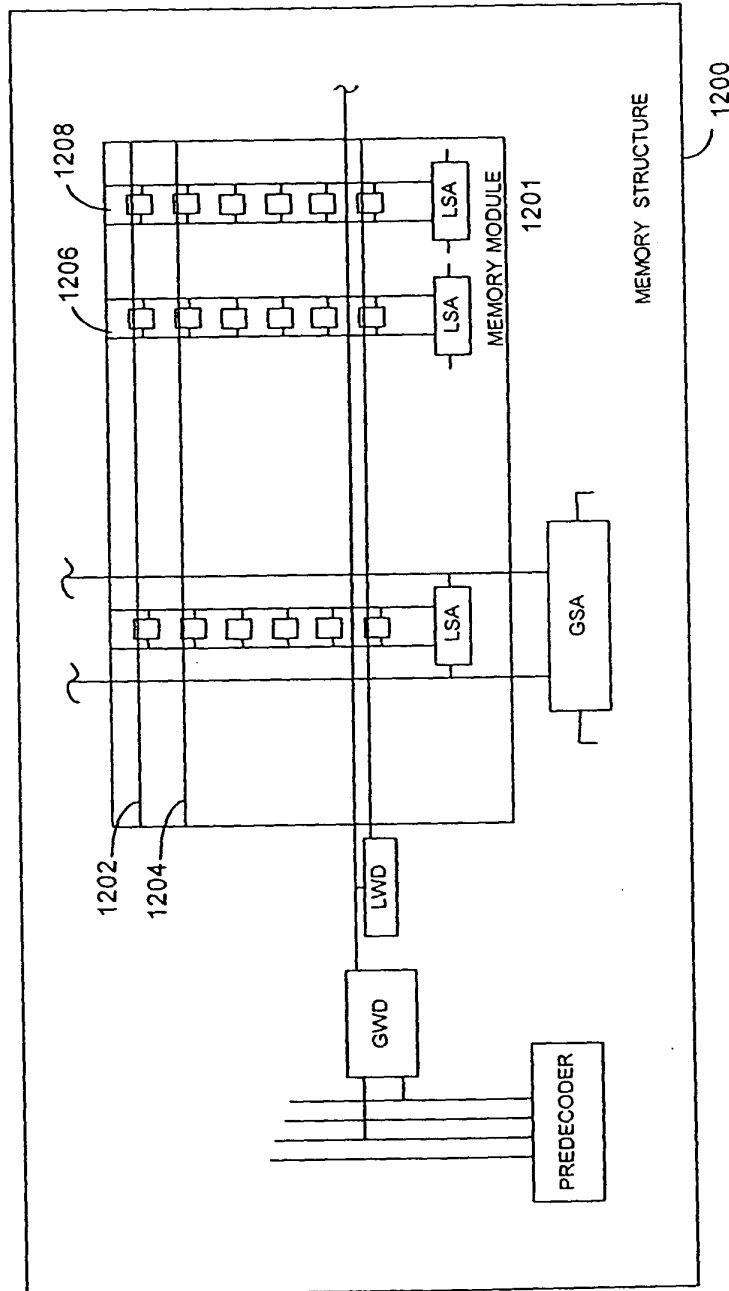


FIG. 11



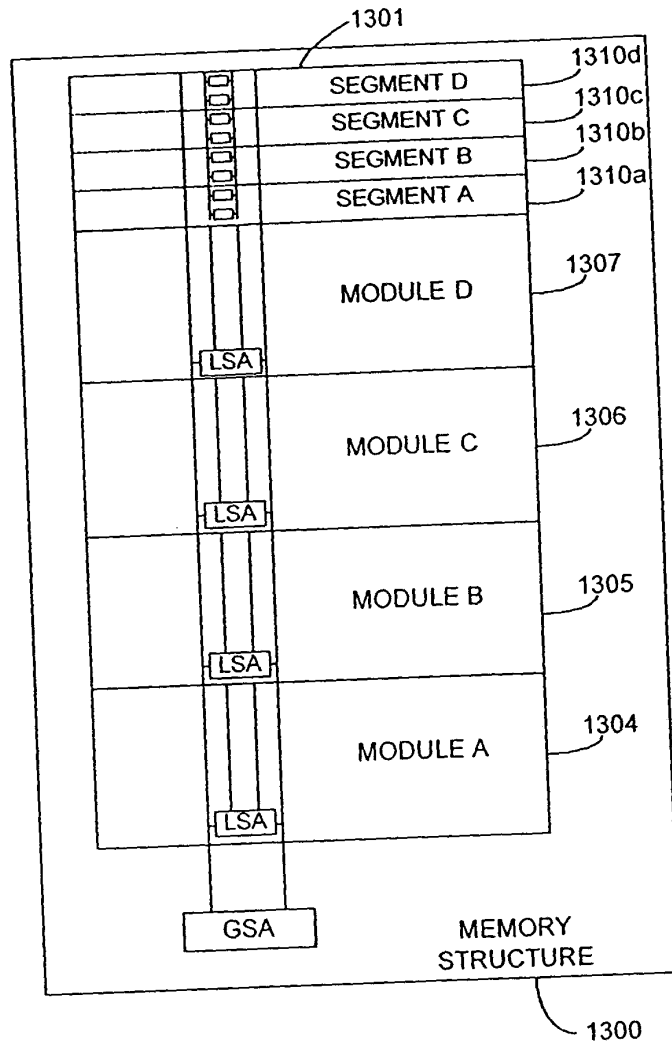


FIG. 13

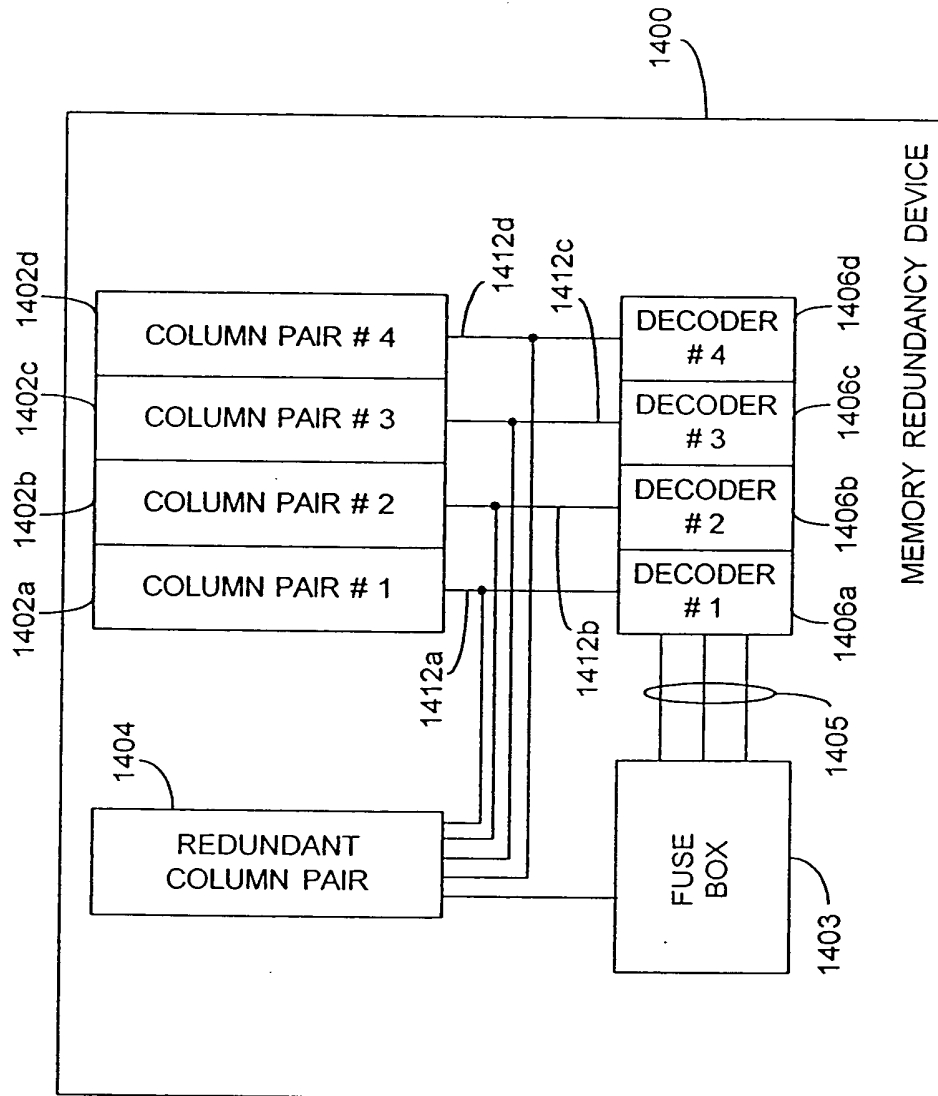


FIG. 14

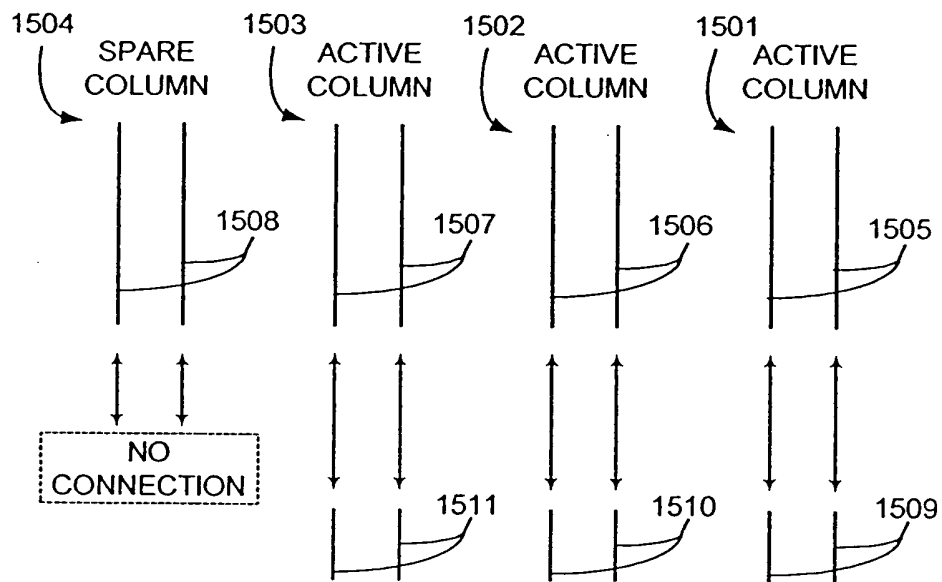


FIG. 15A

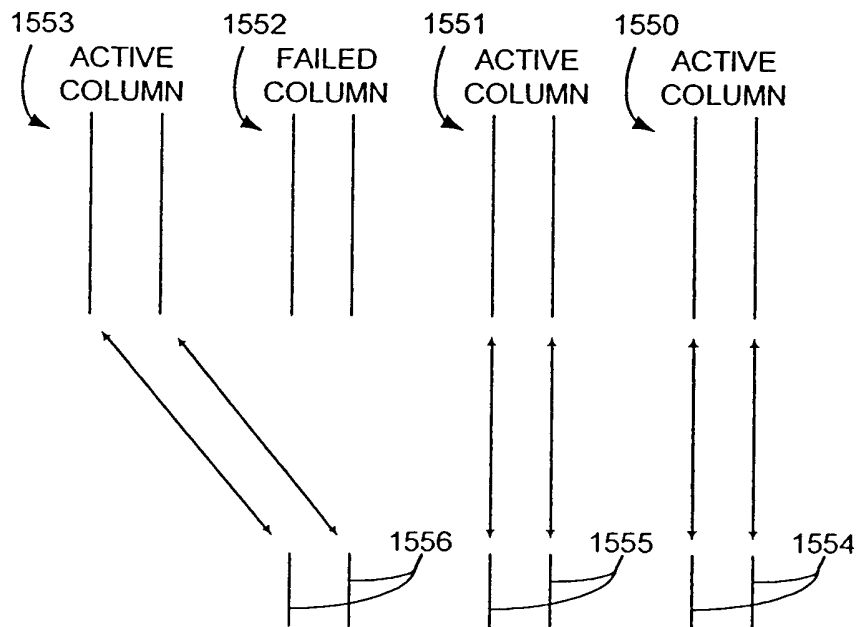


FIG. 15B

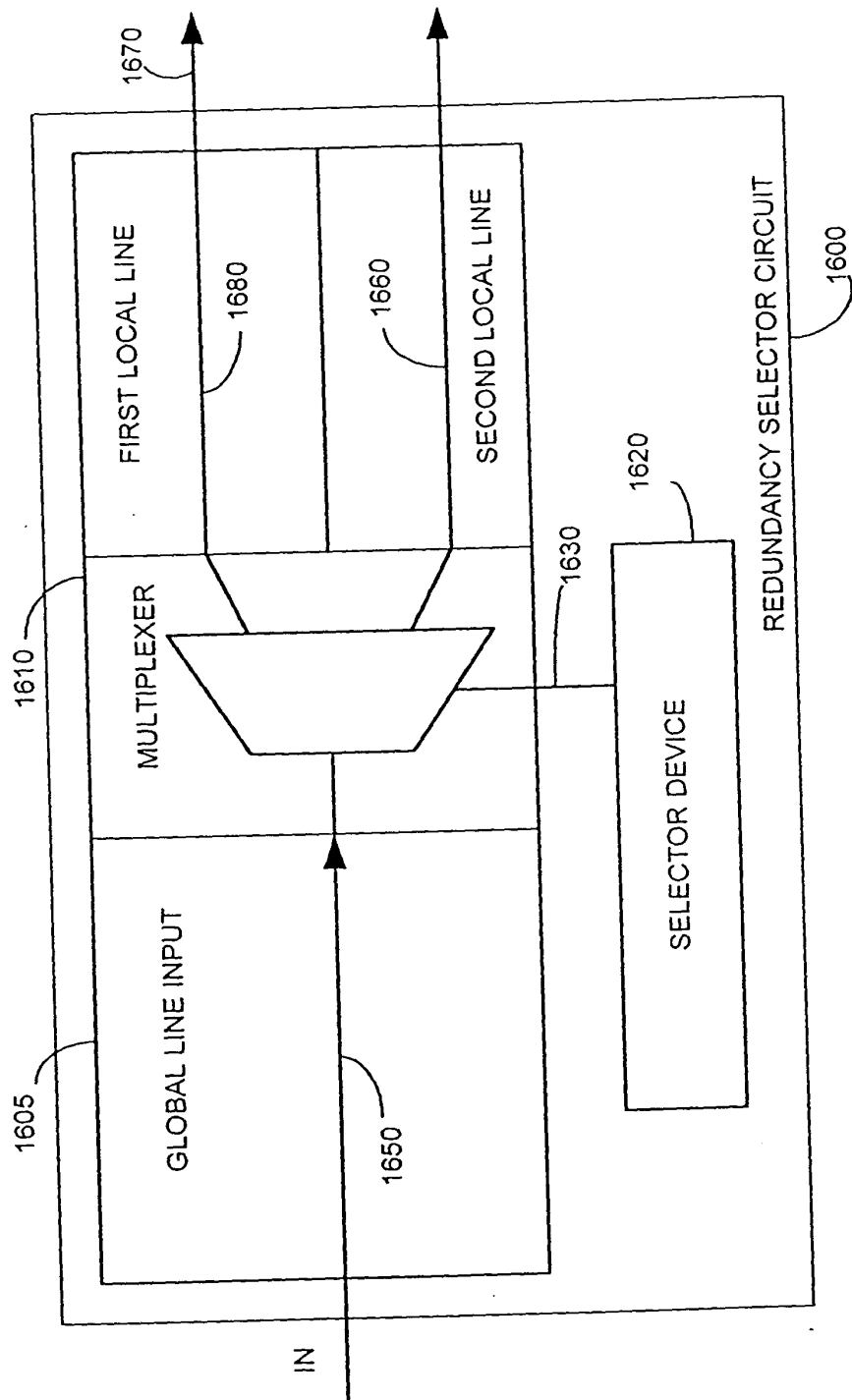


FIG. 16

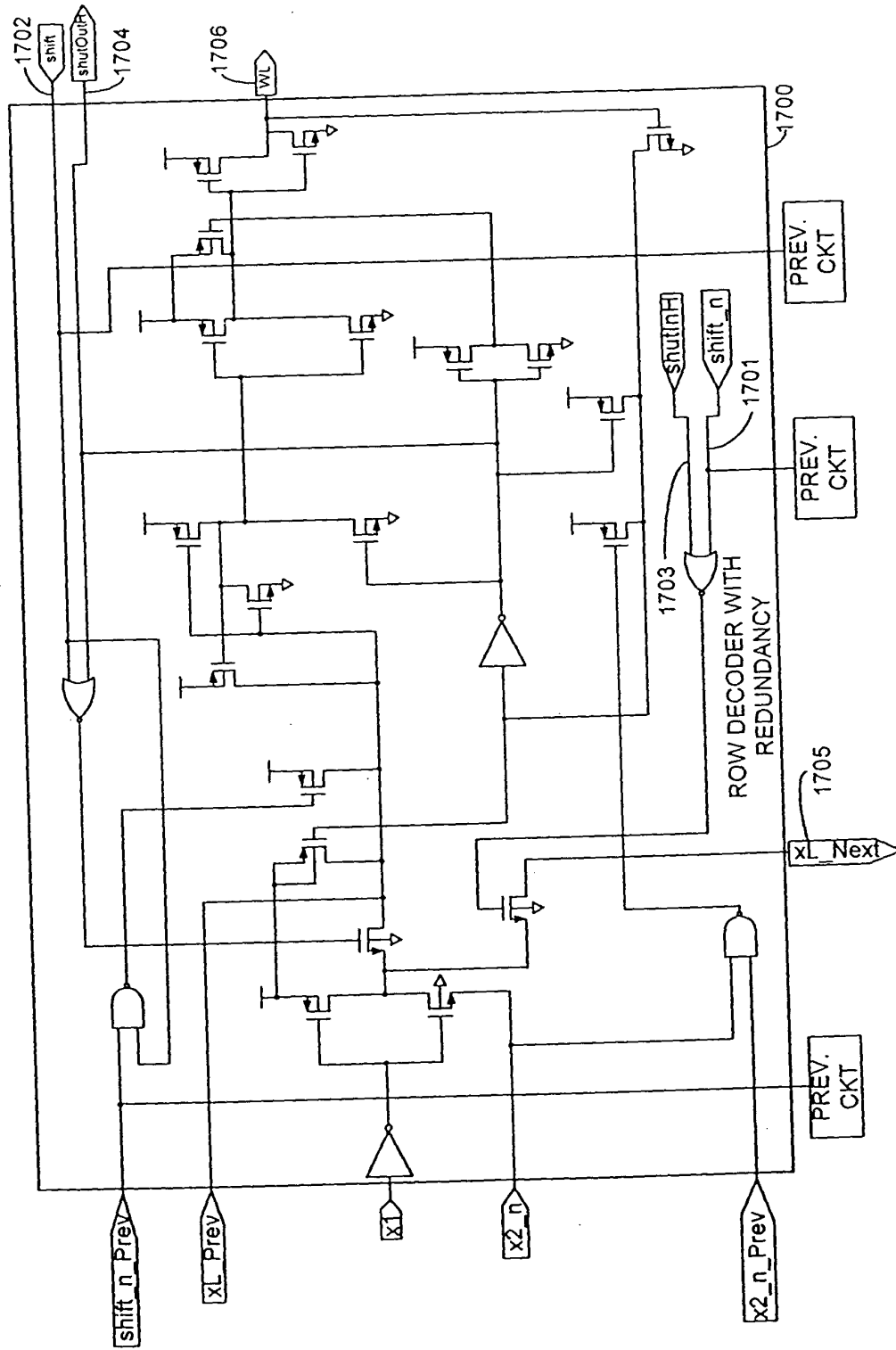


FIG. 17

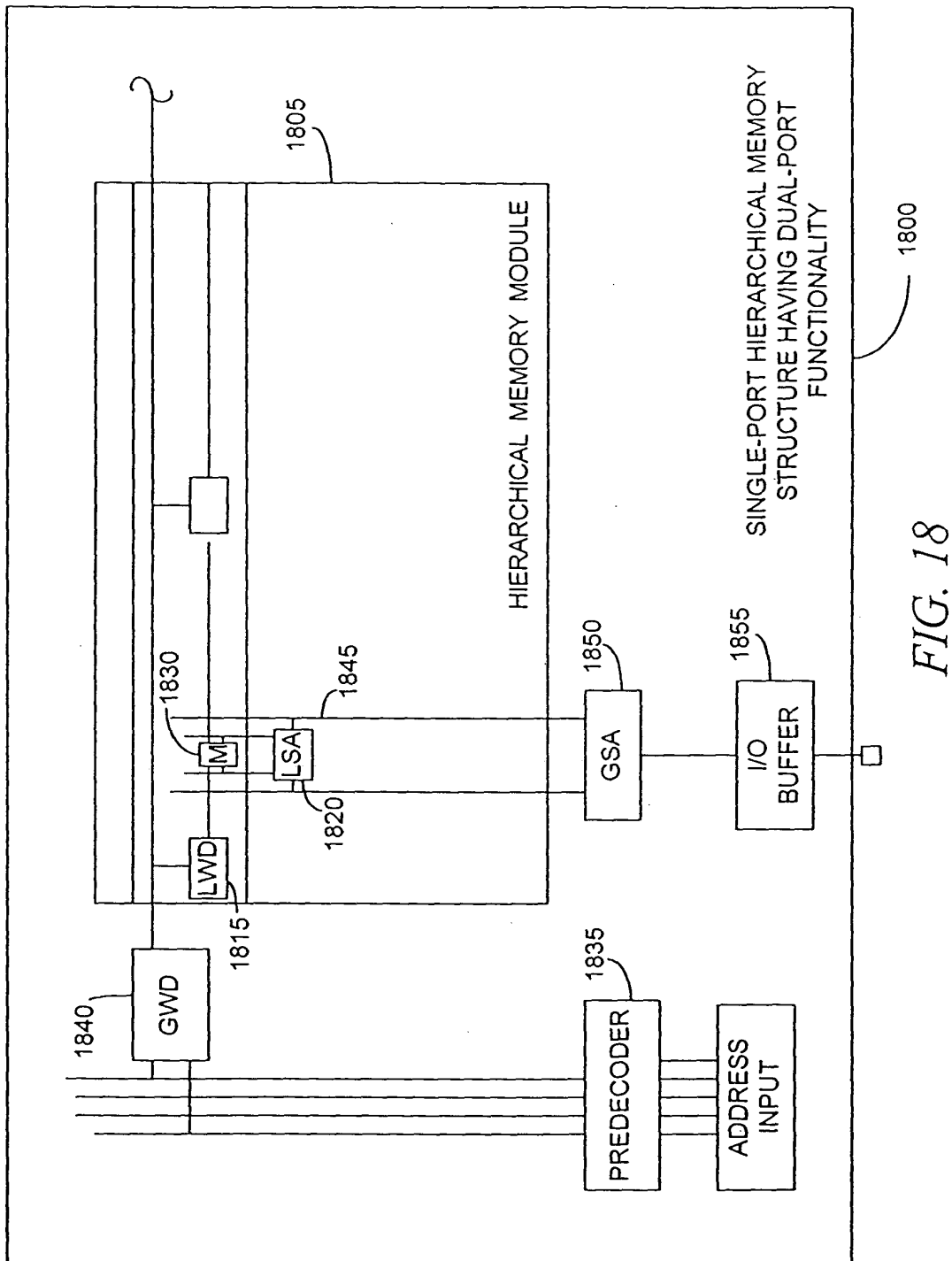


FIG. 18

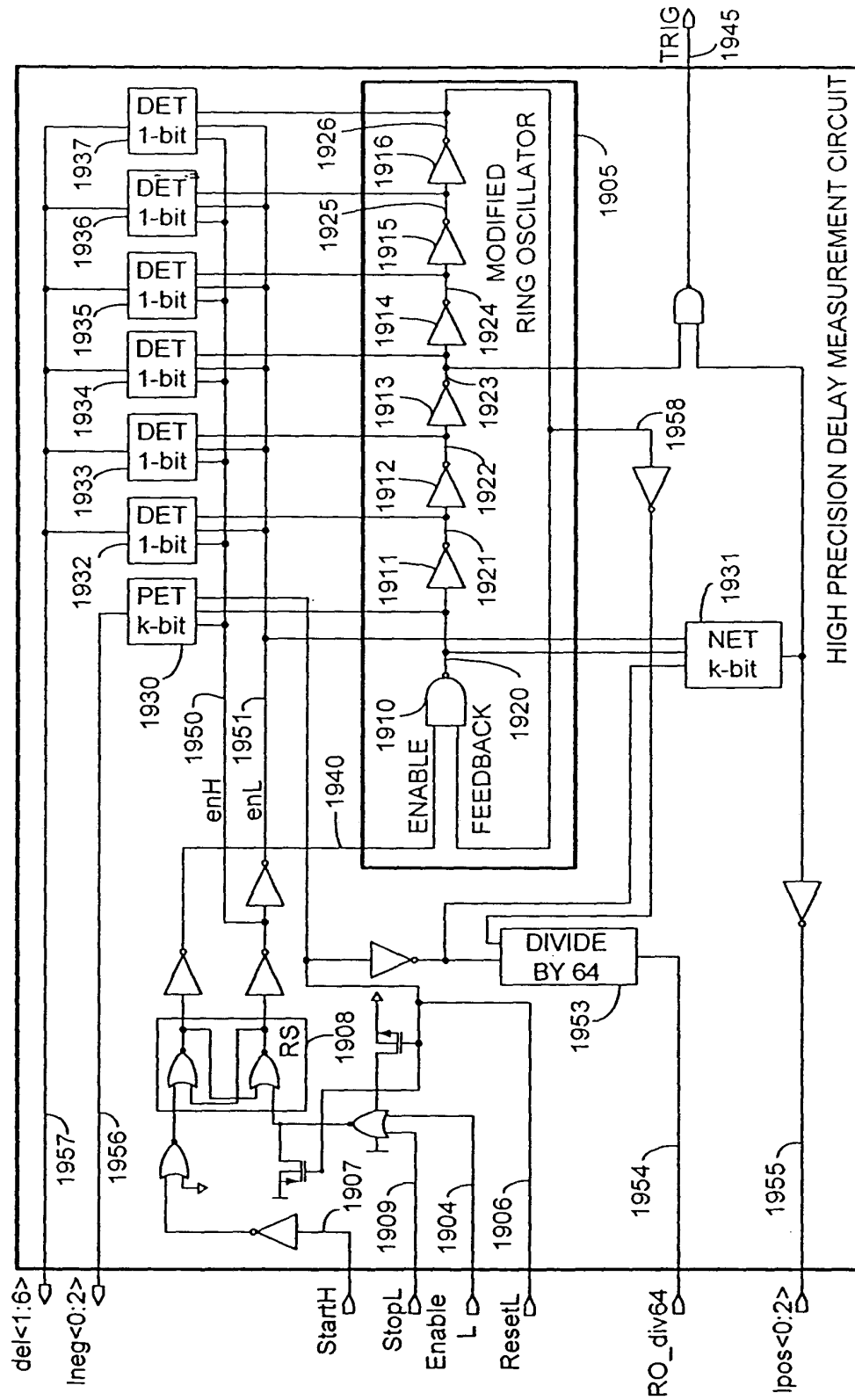


FIG. 19

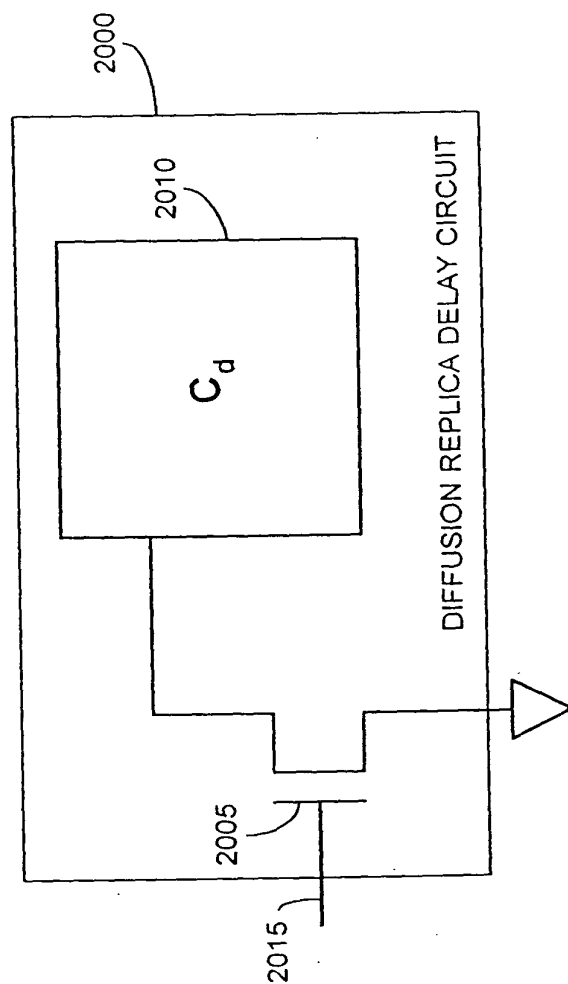


FIG. 20

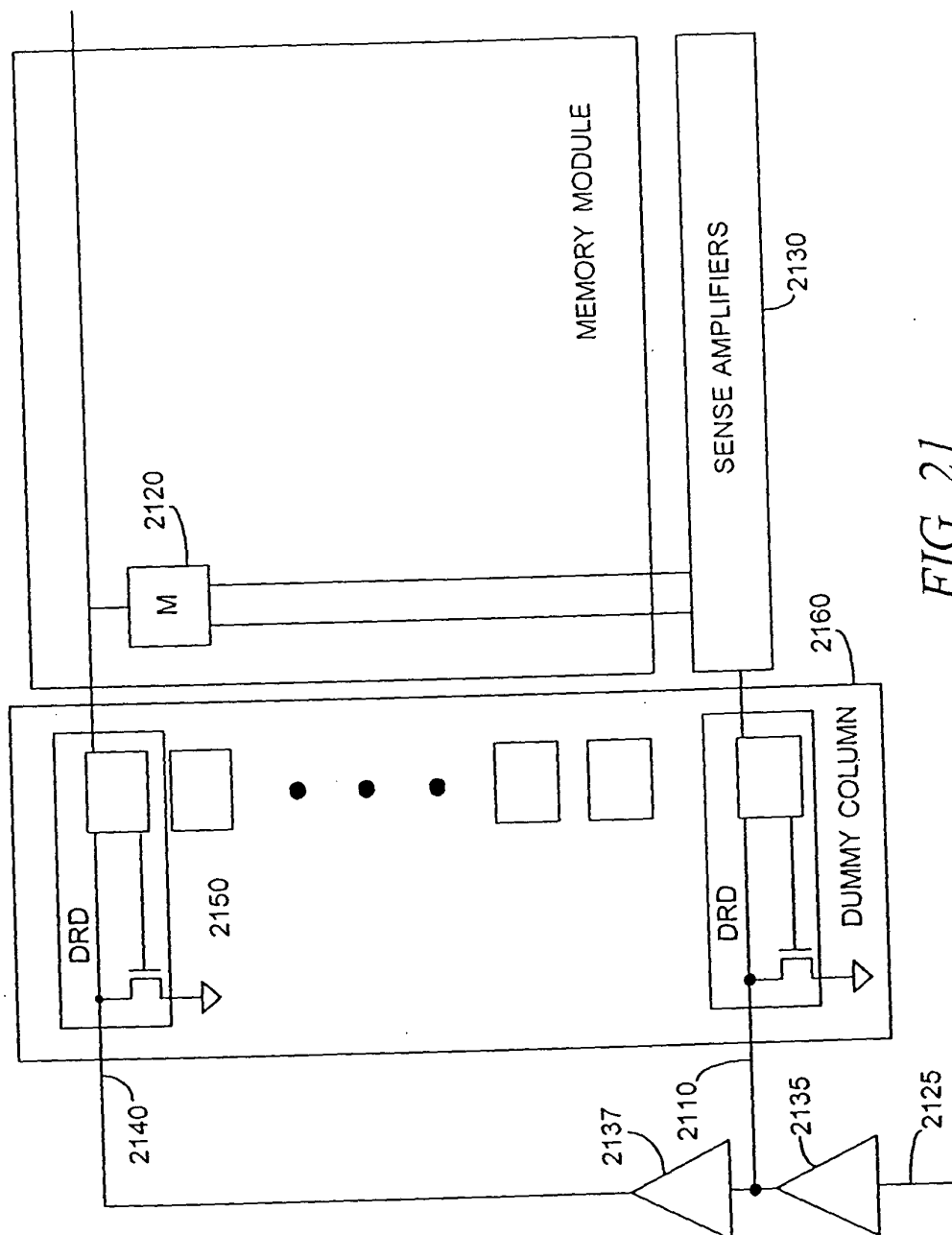


FIG. 21

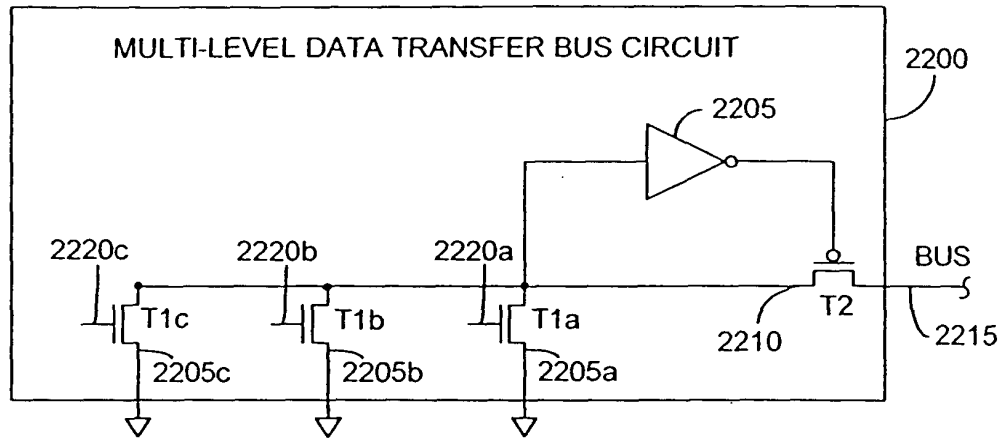


FIG. 22A

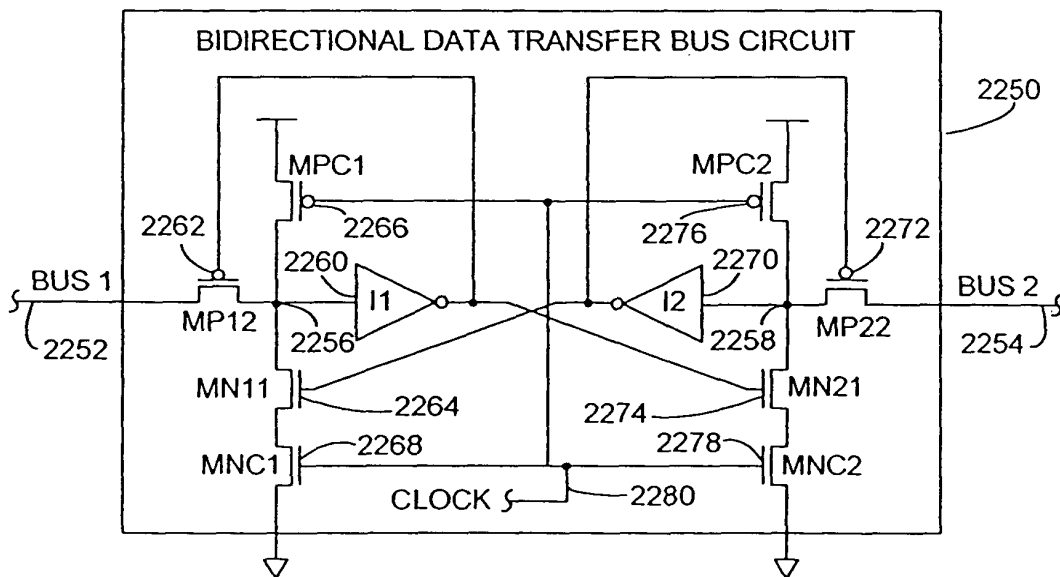


FIG. 22B